

	105 (Current Datasheet)	125 (New Datasheet)
Low Power Mode		
Gain = 1 ²	135	140
Gain = 1 I _{AVDD} Increase per AIN Buffer ²	20	25
Gain = 2 to 8	235	250
Gain = 16 to 128	280	300
I _{AVDD} Increase per Reference Buffer ²	15	20
Mid Power Mode		
Gain = 1 ²	165	170
Gain = 1 I _{AVDD} Increase per AIN Buffer ²	35	40
Gain = 2 to 8	325	345
Gain = 16 to 128	405	430
I _{AVDD} Increase per Reference Buffer ²	30	30
Full Power Mode		
Gain = 1 ²	345	350
Gain = 1 I _{AVDD} Increase per AIN Buffer ²	125	135
Gain = 2 to 8	790	830
Gain = 16 to 128	1100	1200
I _{AVDD} Increase per Reference Buffer ²	110	120
Due to Internal Reference ²	65	70
Due to V _{BIAS} ²	20	20
Due to Diagnostics ²	5	5
I _{IOVDD}		
Low Power Mode	35	35
Mid Power Mode	40	40
Full Power Mode	85	80
Standby Current		
I _{AVDD}	12	15
I _{IOVDD}	17	20
Power-Down Current		
I _{AVDD}	3	3
I _{IOVDD}	2	2